

## Electronic Patent Application Fee Transmittal

<b>Application Number:</b>	10722193			
<b>Filing Date:</b>	26-Nov-2003			
<b>Title of Invention:</b>	INTEGRATED CIRCUIT DEVICES HAVING BURIED INSULATION LAYERS AND METHODS OF FORMING THE SAME			
<b>First Named Inventor:</b>	Byeong-chan Lee			
<b>Filer:</b>	Robert N. Crouse/Candi Riggs			
<b>Attorney Docket Number:</b>	5649-1225			
Filed as Large Entity				
<b>Utility Filing Fees</b>				
<b>Description</b>	<b>Fee Code</b>	<b>Quantity</b>	<b>Amount</b>	<b>Sub-Total in USD(\$)</b>
<b>Basic Filing:</b>				
<b>Pages:</b>				
<b>Claims:</b>				
<b>Miscellaneous-Filing:</b>				
<b>Petition:</b>				
<b>Patent-Appeals-and-Interference:</b>				
Post-Allowance-and-Post-Issuance:				
Utility Appl issue fee	1501	1	1400	1400
Publ. Fee- early, voluntary, or normal	1504	1	300	300

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
Extension-of-Time:				
Miscellaneous:				
Total in USD (\$)				1700